

| | Type | L # | Hits | Search Text | DBs | Time Stamp | Comments |
|---|------|-----|-------|--|--|---------------------|----------|
| 1 | BRS | L1 | 22250 | ((substrate or pcb or board) with (chip or circuit)) and (pins or terminal or contact) and electrical and input and output and housing | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/26 12:01 | |
| 2 | BRS | L2 | 1387 | 1 and 439/\$.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/26 12:02 | |
| 3 | BRS | L3 | 196 | 2 and integrated and chip | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/26 12:02 | |

10/658,431

9/8/03

12/4/02